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Substitute for form 1449A/PTO				Application Number		Filed Herewith 10/003602	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Filing Date		11/14/2001	
				First Named Inventor:		Wingyu Leung	
				Examiner name: unknown		GROUP: unknown	
Sheet	1	of	1	Attorney Docket Number		MST-012	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cited No. <sup>1</sup>	U.S. Patent Document Number      Kind Code <sup>2</sup> (if known)		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
	A1	5003542		Mashiko et al.	03/26/1991	
	A2	5638385		Fifield et al.	06/10/1997	
	A3	6065146		Bosshart	05/16/2000	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cited No. <sup>1</sup>	Foreign Patent Document Office <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear	T <sup>6</sup>

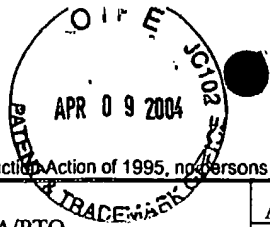
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
10/003602	"16-Bit CMOS Error Detection And Correction Unit", ©1990 Integrated Device Technology, Inc., pages 1-19
↓	"Cost Analysis of On Chip Error Control Coding for Fault Tolerant Dynamic RAMs", by Najmi Jarwala and Dhiraj K. Pradham, 1987 IEEE, pages 278-283
↓	"A Class of Optimal Minimum Odd-weight-column SEC-DED Codes" by M.Y. Hsiao, July 1970, pages 395-401

EXAMINER <i>Christine</i>	DATE CONSIDERED 8/6/00
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	Examiner name: A. Decady	GROUP: 2133

Sheet	1	of	1	Attorney Docket Number	MST-012
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<i>WLS</i>	C1	US-4380812	04-19-1983	Ziegler, II et al.	
↓	C2	US-4694454	09-15-1987	Matsuura	
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		US-			
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		US-			
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<i>WLS</i>	C3	JP 55055499	04-23-1980	NEC Corporation		

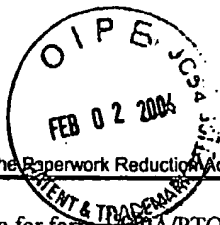
**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


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PTO/SB/08A (08-03)

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CJK	B1	US-5,524,220	06-04-1996	Verma et al.	
↓	B2	US-6,147,535	11-14-2000	Leung	
		US-			
		US-			
		US-			
		US-			

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CJK	B3	GB 2289779	11-29-1995	Intel Corporation		

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

CJK	"Error detection and correction with the IDT49C466" by Anupama Hegde, 12/18/1994, No. 10, pages 613-620.
	"Error Correction Circuitry for Dynamic Random Access Memories", IBM Technical Disclosure Bulletin, Vol. 36, No. 08, August 1993, pages 681-682.

EXAMINER SIGNATURE: *Christina*

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